EUROPEAN SEARCH REPORT

Application Number EP 02 25 7509

Catacas	Citation of document with indication, where appropriate,	Relevant	CLASSIFICATION OF THE
Category	of relevant passages	to claim	APPLICATION (Int.C1.7)
Χ	WO 01 93431 A (QUALCOMM INC)	1-8	H04L1/00
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	* figures 1,2 *		_
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	* page 1, paragraph 8 - page 2, paragraph		÷
:	10 * * page 3, paragraph 33 - paragraph 36 *		
	* page 7, paragraph 73 - paragraph 75 *		•
	* figures 2,3 *		0
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X.	KATSUHIKO UEDA ET AL: "A 16-BIT DIGITAL	1-8	
	SIGNAL PROCESSOR WITH SPECIALLY ARRANGED MULTIPLY-ACCUMULATOR FOR LOW POWER		
•	CONSUMPTION"		
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•	COMM. ENG. TOKYO, JP,		TECHNICAL FIELDS SEARCHED (Int.CI.7)
	vol. E78-C, no. 12, 1 December 1995 (1995-12-01), pages		H04L
	1709-1716, XP000555577		11042
	ISSN: 0916-8524		
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	The present search report has been drawn up for all claims		
-	Place of search Date of completion of the search	l [:]	Examiner
•	THE HAGUE 8 April 2003	Tou	mpoulidis, T
C	ATEGORY OF CITED DOCUMENTS T: theory or principle	underlying the i	nvention
•	E : earlier patent doc cularly relevant if taken alone after the filing dat	ument, but public	
Y : parti	cularly relevant if combined with another D: document cited in ment of the same category L: document cited for	the application	
A:tech	nological background		
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